

Micron engineering candidate guide

Micron is looking for the brightest minds to join our international team of innovators. Match your education and experience to see what jobs might be a fit for you.



Department	Job title(s)	Education	Degree	Preferred coursework	Job location(s)	
Process Development/ Equipment R&D	<ul style="list-style-type: none"> Process development engineer Process pathfinding engineer Equipment R&D engineer Areas: Photolithography, Dry etch, Wet process, CMP, Thin films, Implant, Metrology, and RDA 	<ul style="list-style-type: none"> MS PhD 	<ul style="list-style-type: none"> Chemistry/Chemical engineering Electrical engineering Microelectronics engineering Materials science Physics Mechanical engineering Mechatronics 	<ul style="list-style-type: none"> Device or processing research Semiconductor processing lab Data science application in engineering Statistics Design of experiments (DOE) 	<ul style="list-style-type: none"> Computational fluid dynamics Computational materials Thermo-mechanical modeling AI assisted modeling Optics 	<ul style="list-style-type: none"> Boise, ID
Process Integration R&D	<ul style="list-style-type: none"> Process integration engineer Device engineer CMOS engineer 	<ul style="list-style-type: none"> MS PhD 	<ul style="list-style-type: none"> Chemistry/Chemical engineering Electrical engineering Microelectronics engineering Materials science Physics 	<ul style="list-style-type: none"> Semiconductor physics Device or processing research CMOS Design of experiments (DOE) 	<ul style="list-style-type: none"> SPC, JMP Circuit design Matlab, SQL, Python, Java, or other data science coursework 	<ul style="list-style-type: none"> Boise, ID
Mask Technology Center	<ul style="list-style-type: none"> Process development engineer Reticle manufacturing engineer R&D process engineer Process integration engineer Yield engineer 	<ul style="list-style-type: none"> BS MS PhD 	<ul style="list-style-type: none"> Chemistry/Chemical engineering Mechanical engineering Materials science Mathematics/statistics Physics 	<ul style="list-style-type: none"> Semiconductor physics Statistics Optics 	<ul style="list-style-type: none"> SPC, Design of experiments (DOE), JMP C++, SQL, Python, Java 	<ul style="list-style-type: none"> Boise, ID
Central Engineering	<ul style="list-style-type: none"> CAD engineer Competitor analysis engineer EMI/EMC engineer Lab engineer PCB design & test engineer Signal integrity engineer Data scientist 	<ul style="list-style-type: none"> BS MS PhD 	<ul style="list-style-type: none"> Chemistry/Chemical engineering Computer engineering Computer engineering Electrical engineering Mechanical engineering Thermal engineering EHSQ Materials science Physics 	<ul style="list-style-type: none"> Device physics EDA Electromagnetics Electron microscopy Lab testing equipment Mechanical understanding of tools 	<ul style="list-style-type: none"> PCB design VLSI design Programming Statistics 	<ul style="list-style-type: none"> Atlanta, GA Austin, TX Dallas, TX Boise, ID Folsom, CA San Jose, CA Longmont, CO Minneapolis, MN
Global Quality	<ul style="list-style-type: none"> Manufacturing quality engineer Quality data analyst engineer Quality product reliability engineer 	<ul style="list-style-type: none"> BS MS PhD 	<ul style="list-style-type: none"> Computer engineering Electrical engineering Mechanical engineering 	<ul style="list-style-type: none"> CMOS logic Manufacturing Project management Semiconductor physics 	<ul style="list-style-type: none"> Statistics C++ VLSI design 	<ul style="list-style-type: none"> Boise, ID Longmont, CO
Test and Probe	<ul style="list-style-type: none"> Test engineer Equipment engineer Process engineer Manufacturing engineer Software engineer 	<ul style="list-style-type: none"> BS MS 	<ul style="list-style-type: none"> Computer engineering Electrical engineering Industrial engineering Mechanical engineering Computer science 	<ul style="list-style-type: none"> Digital logic Electrical/electronics/mechanics Operations/project management Systems engineering 	<ul style="list-style-type: none"> Programming: Python, C++, Perl Statistics Design of experiments (DOE) UNIX CAD 	<ul style="list-style-type: none"> Boise, ID Clay, NY (coming soon)

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Advanced Packaging Technology Development	<ul style="list-style-type: none"> • Package development engineer • Semiconductor packaging engineer • Process development engineer • Process intergration engineer • Package modeling • Substrate designer 	<ul style="list-style-type: none"> • MS • PhD 	<ul style="list-style-type: none"> • Chemistry/Chemical engineering • Electrical engineering • Microelectronics engineering • Materials science • Mechanical engineering • Mechatronics/Physics • Metallurgical engineering • Polymer science 	<ul style="list-style-type: none"> • Semiconductor packaging • Device or processing research • Semiconductor processing lab • Data science application in engineering • Thermal modeling • Mechanical modeling 	<ul style="list-style-type: none"> • Statistics • Design of experiments (DOE) • Computational fluid dynamics • Computational materials • Thermo-mechanical modeling • AI assisted modeling 	<ul style="list-style-type: none"> • Boise, ID
HBM/ DRAM Engineering	<ul style="list-style-type: none"> • Design engineer • Product engineer • Verification/validation engineer • Layout engineer 	<ul style="list-style-type: none"> • BS • MS • PhD 	<ul style="list-style-type: none"> • Electrical engineering • Computer engineering 	At least one from: <ul style="list-style-type: none"> • CMOS logic • Digital logic • VLSI design • Analog circuit design • RTL circuit design 	<ul style="list-style-type: none"> • C++ • C# • Semiconductor physics 	<ul style="list-style-type: none"> • Atlanta, GA • Boise, ID • Folsom, CA • Richardson, TX
Firmware Development	<ul style="list-style-type: none"> • Firmware architect • Firmware developer • Firmware test architect • Firmware tester 	<ul style="list-style-type: none"> • BS • MS • PhD 	<ul style="list-style-type: none"> • Computer engineering • Electrical engineering 	<ul style="list-style-type: none"> • Programming projects in C/C++ • Digital design • Digital devices • Control theory 	<ul style="list-style-type: none"> • Computer architectures • Digital signal processing • Real time systems 	<ul style="list-style-type: none"> • Boise, ID • Folsom, CA, • San Jose, CA, • Longmont, CO • Minneapolis, MN
Nonvolatile Engineering	<ul style="list-style-type: none"> • Design engineer • Product engineer • Verification engineer • System engineer • Characterization engineer • Yield engineer 	<ul style="list-style-type: none"> • BS • MS • PhD 	<ul style="list-style-type: none"> • Computer engineering • Electrical engineering 	At least one from: <ul style="list-style-type: none"> • CMOS logic • Digital logic • VLSI design 	<ul style="list-style-type: none"> • Programming language: C++, C#, Java, etc. • Semiconductor physics • Analog circuits 	<ul style="list-style-type: none"> • Boise, ID • San Jose, CA, • Longmont, CO
SSD Engineering	<ul style="list-style-type: none"> • IC design engineer • Verification/validation engineer 	<ul style="list-style-type: none"> • BS • MS • PhD 	<ul style="list-style-type: none"> • Computer engineering • Electrical engineering 	<ul style="list-style-type: none"> • Computer architecture • VLSI design 	<ul style="list-style-type: none"> • Digital logic design • Verilog/VHDL/ SystemVerilog 	<ul style="list-style-type: none"> • Boise, ID • San Jose, CA • Longmont, CO
Manufacturing/ Operations	<ul style="list-style-type: none"> • Manufacturing engineer • Process engineer • Proces integration engineer • Equipment engineer • EHS engineer • Facilities engineer • Industrial engineer 	<ul style="list-style-type: none"> • BS • MS 	<ul style="list-style-type: none"> • Chemistry/Chemical engineering • Electrical engineering • Environmental engineering • Industrial engineering • Mechanical engineering • Microelectronics engineering • Material science • Supply chain management • Physics • Other STEM degrees 	<ul style="list-style-type: none"> • Statistics • SPC • Electromechanical coursework (<i>for equipment engineer role</i>) 	<ul style="list-style-type: none"> • Programming • Semi process lab • Semiconductor materials • Semiconductor physics 	<ul style="list-style-type: none"> • Boise, ID • Manassas, VA • Clay, NY (<i>coming soon</i>)
Information Technology	<ul style="list-style-type: none"> • Software engineer • System manager • Database administrator • Business analyst • Project manager • Software developer 	<ul style="list-style-type: none"> • BS • MS 	<ul style="list-style-type: none"> • Computer engineering • Computer science • Computer information systems • Management information systems 	<ul style="list-style-type: none"> • C++/C#, .Net, Python/ Angular • Cloud, Windows, Unix, Linux 	<ul style="list-style-type: none"> • SQL • SSIS, SSRS • SAP/ABAP 	<ul style="list-style-type: none"> • Boise, ID

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